

FIGURE 1

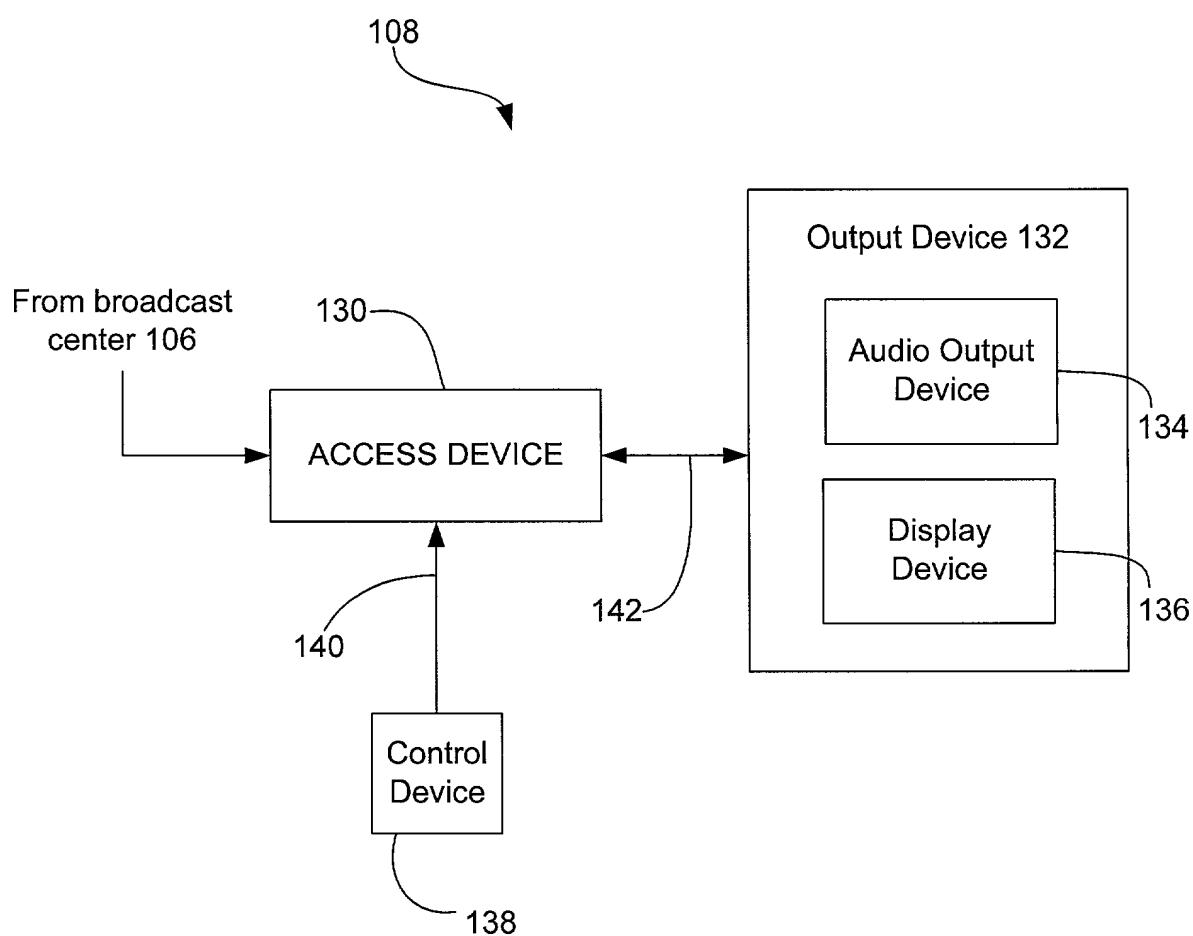


FIGURE 2

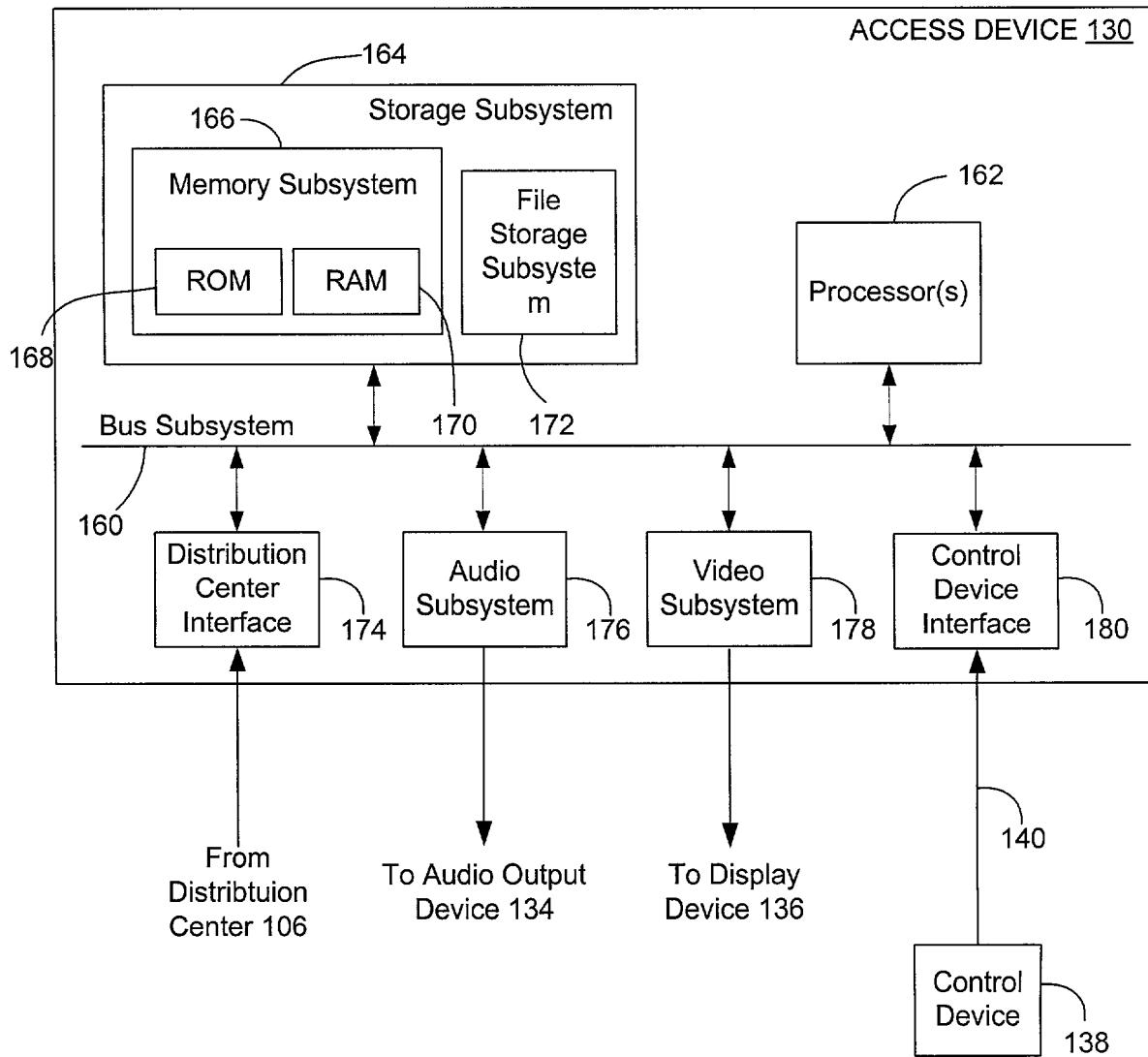


FIGURE 3

150

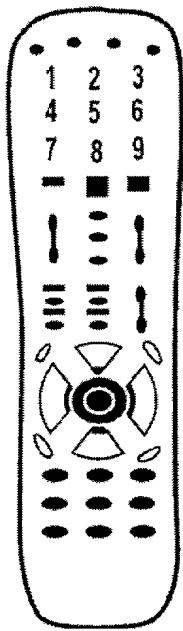


FIGURE 4

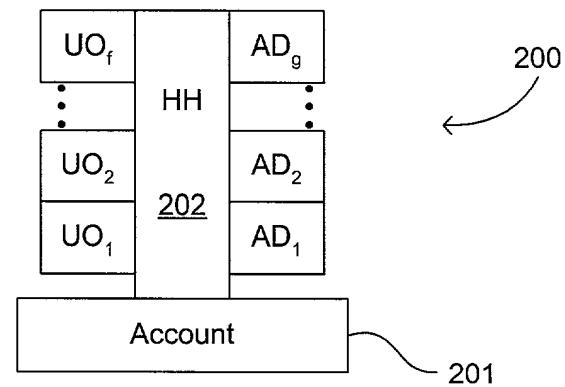


FIGURE 5

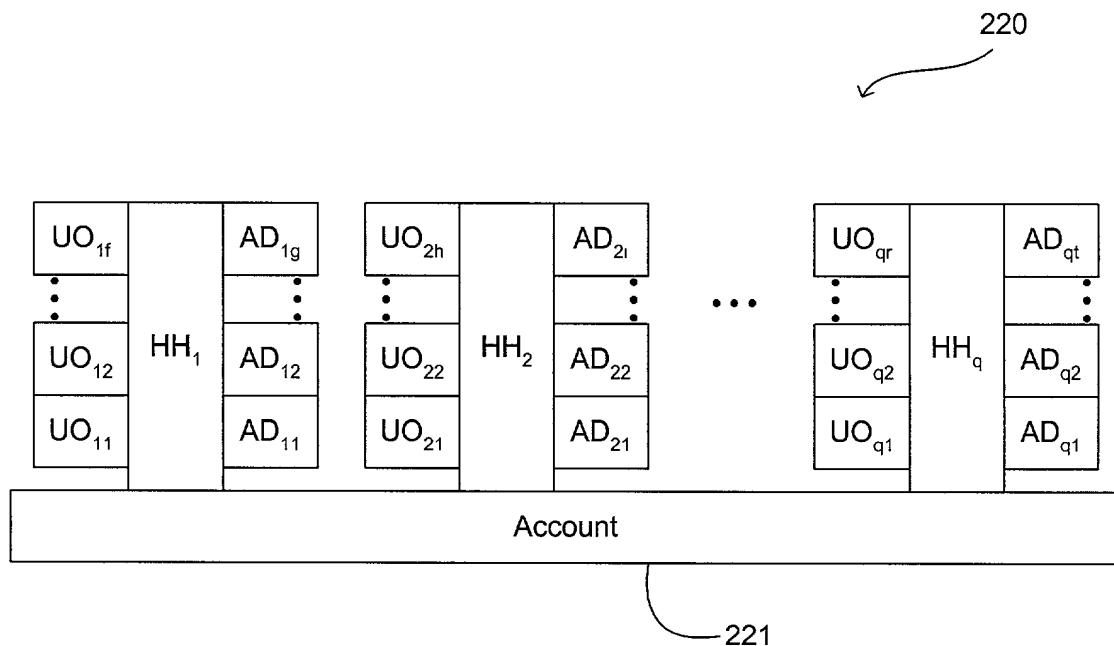


FIGURE 6

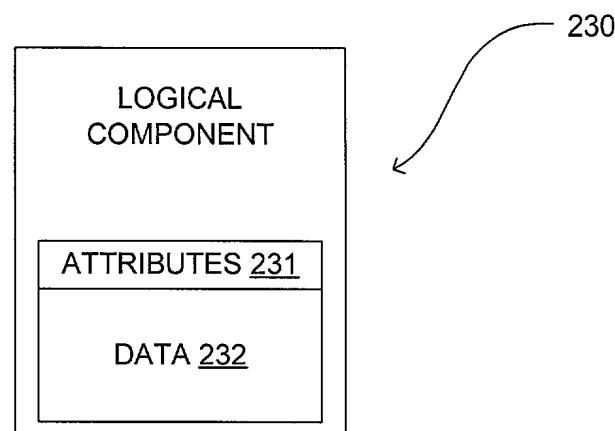


FIGURE 7

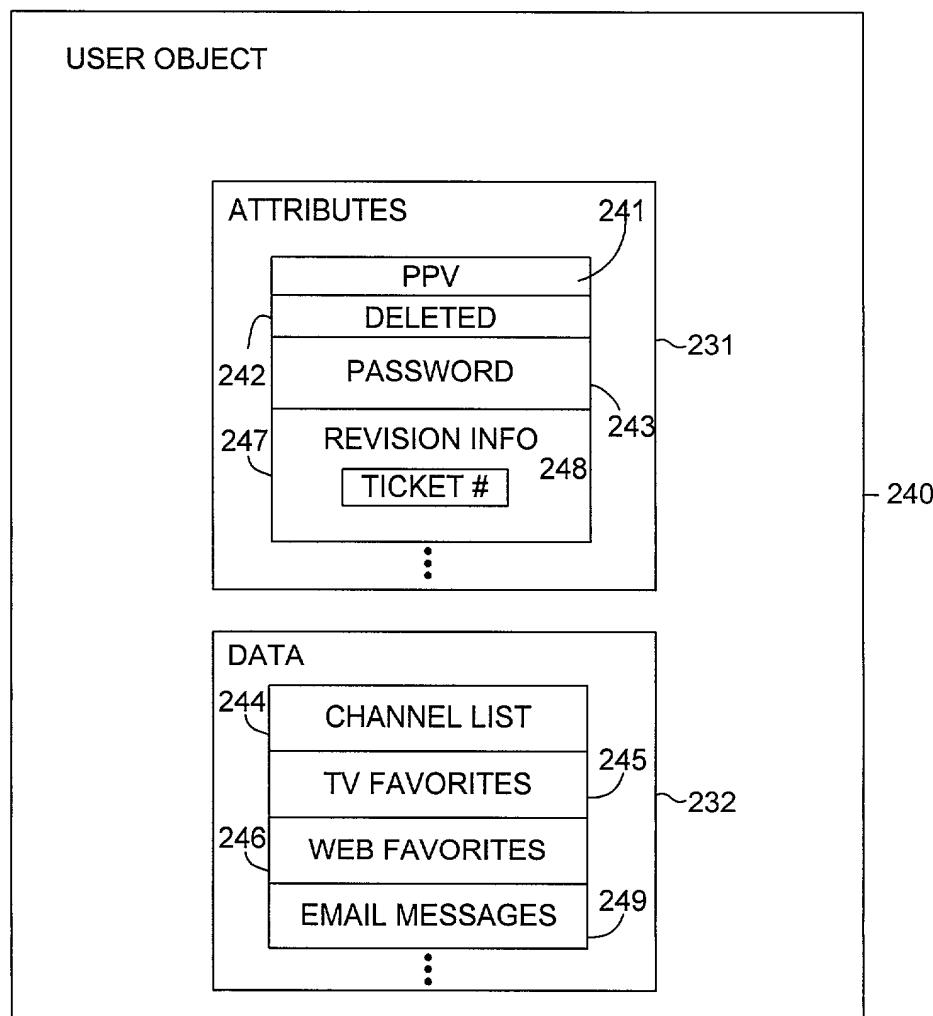


FIGURE 8

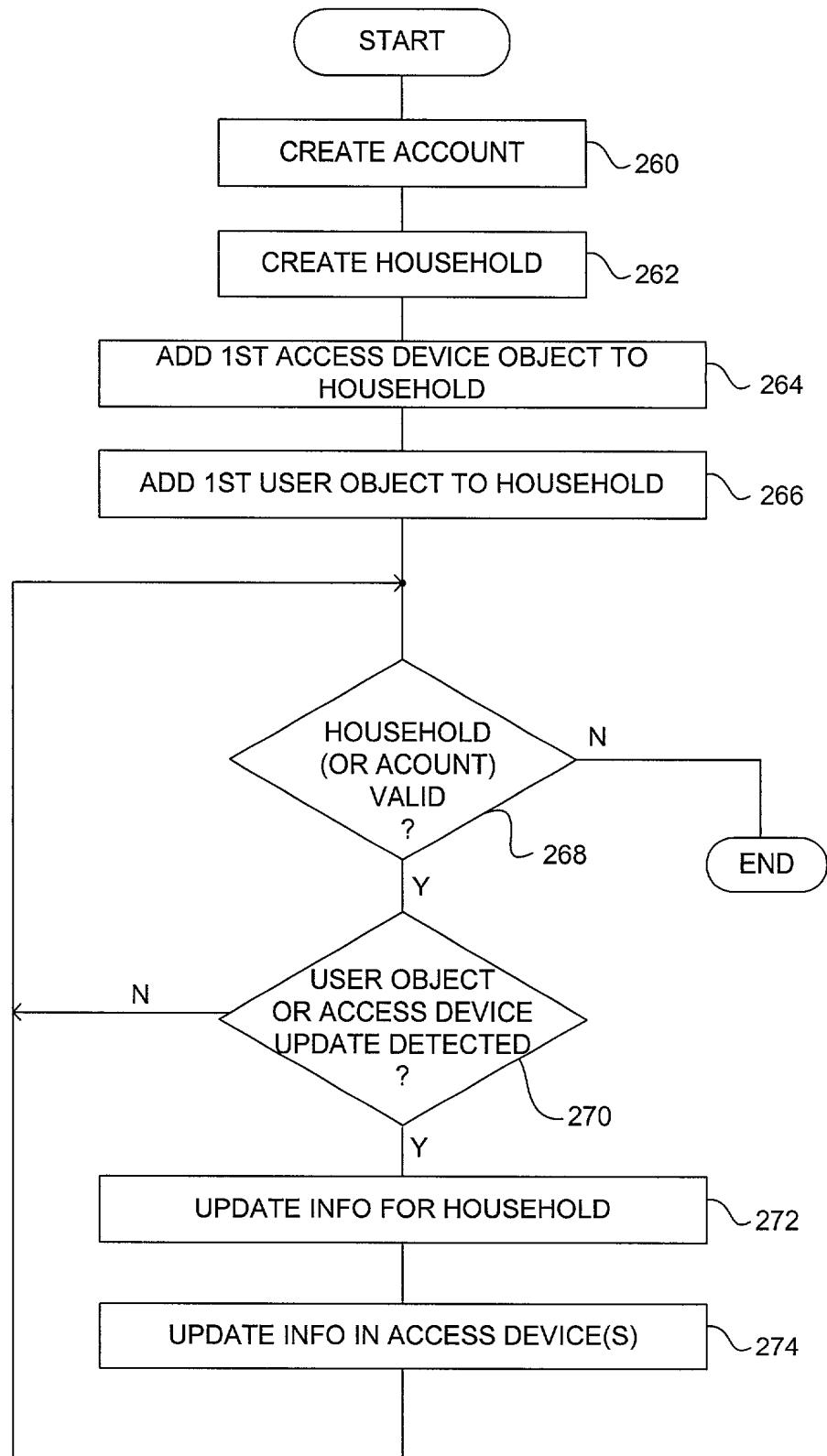


FIGURE 9

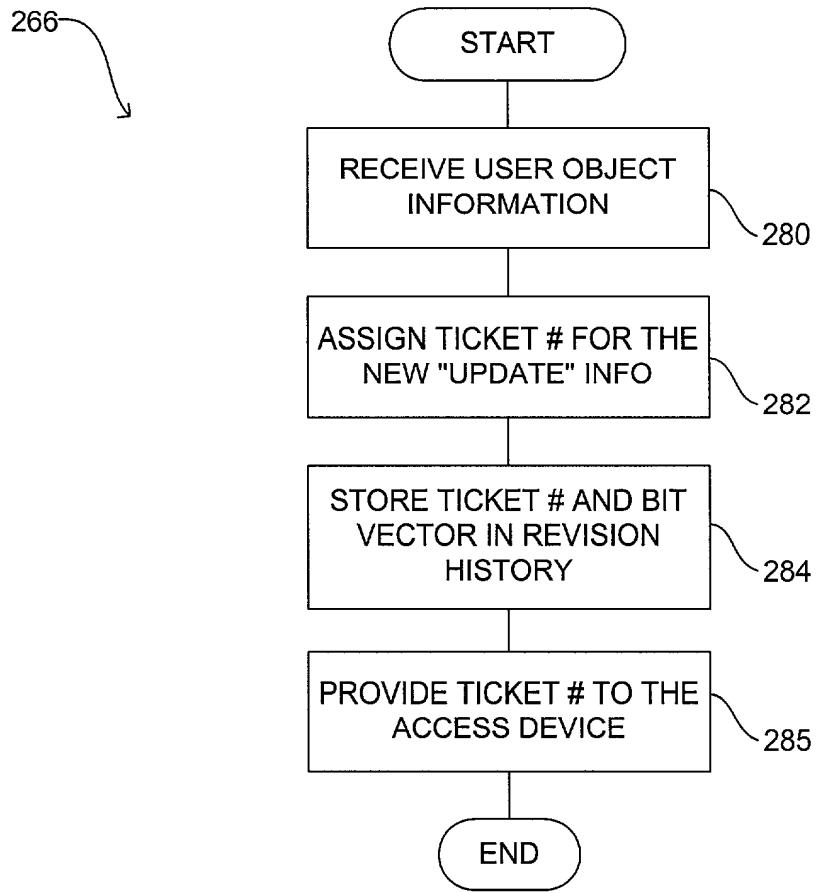


FIGURE 10

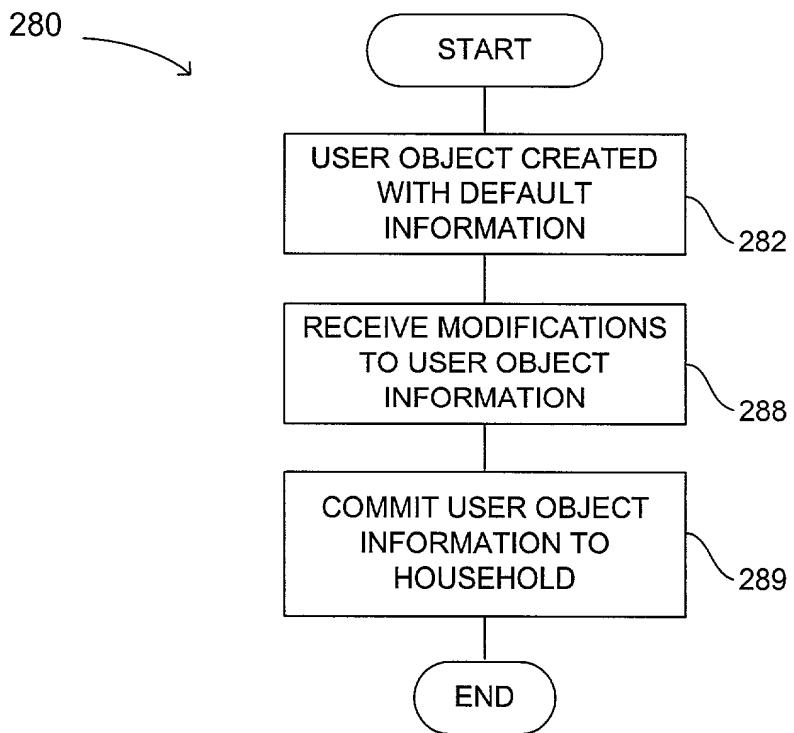


FIGURE 10A

REVISION HISTORY	
TICKET #	BIT VECTOR
X+N	0 0 0 0 0 1 0 0
:	⋮
X+2	0 0 0 0 0 0 1 0
X+1	0 1 0 0 0 0 0 0
X	0 0 0 1 0 0 0 0

FIGURE 11

SERVER

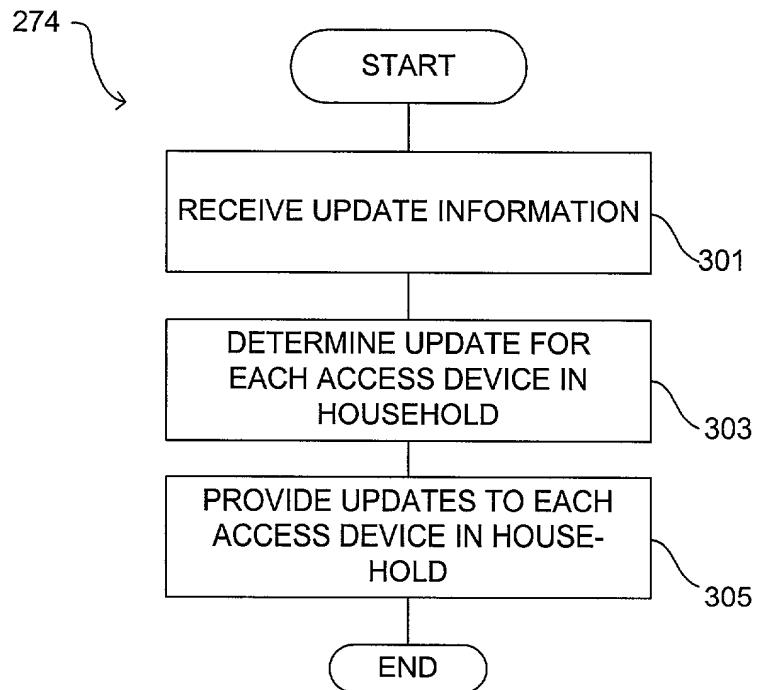


FIGURE 12

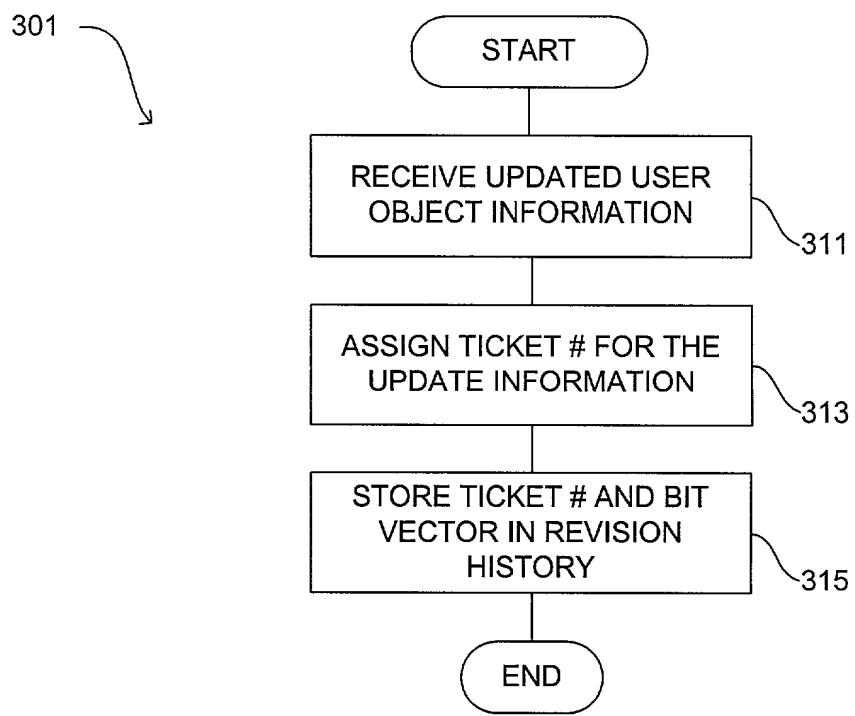


FIGURE 13

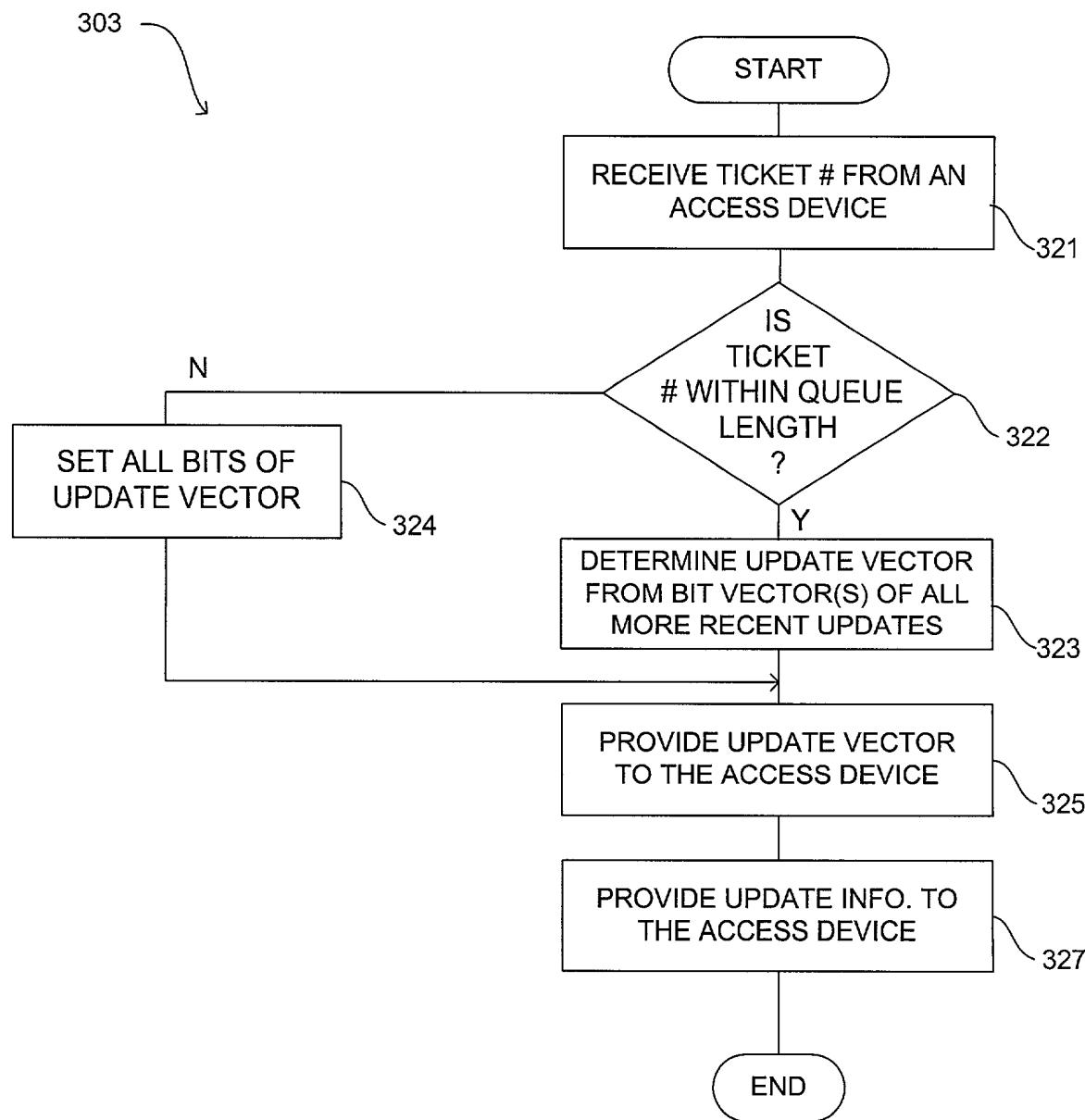


FIGURE 14

DEVICE

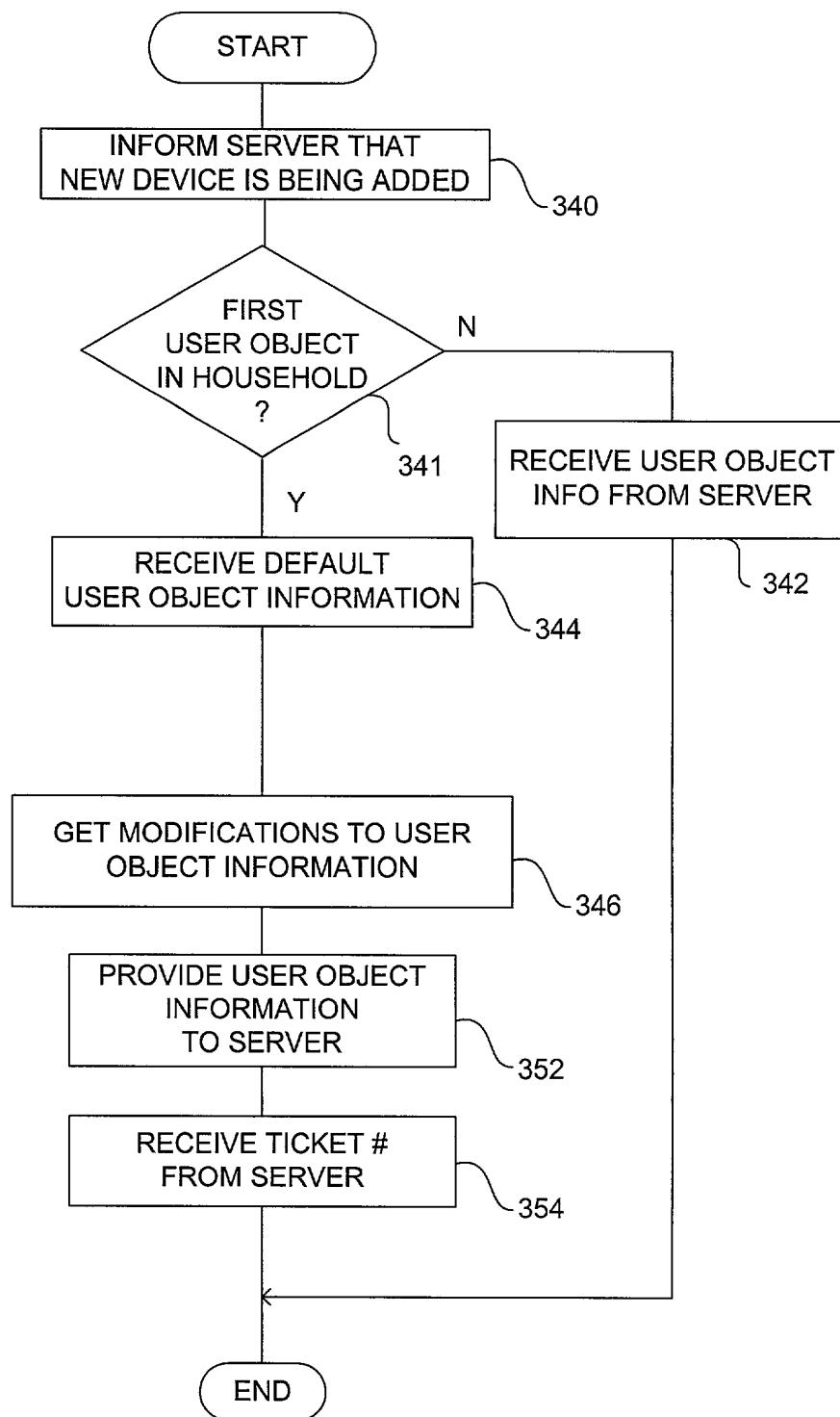


FIGURE 15

DEVICE

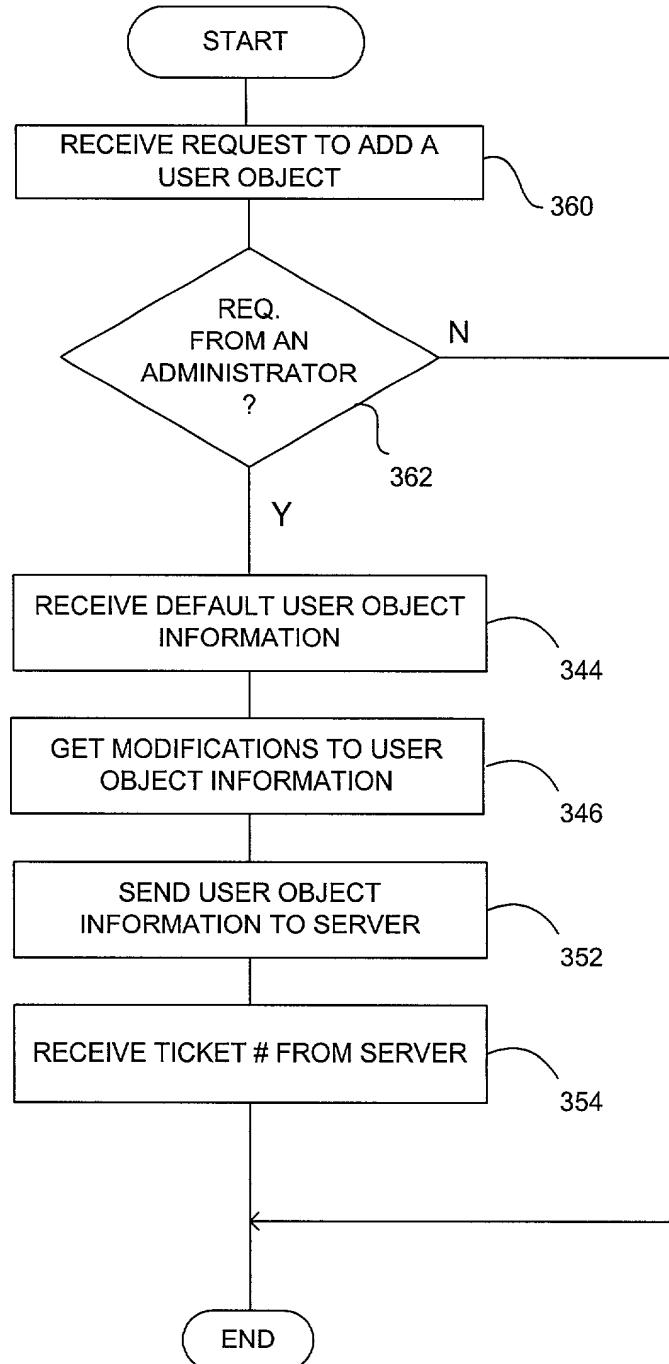


FIGURE 16

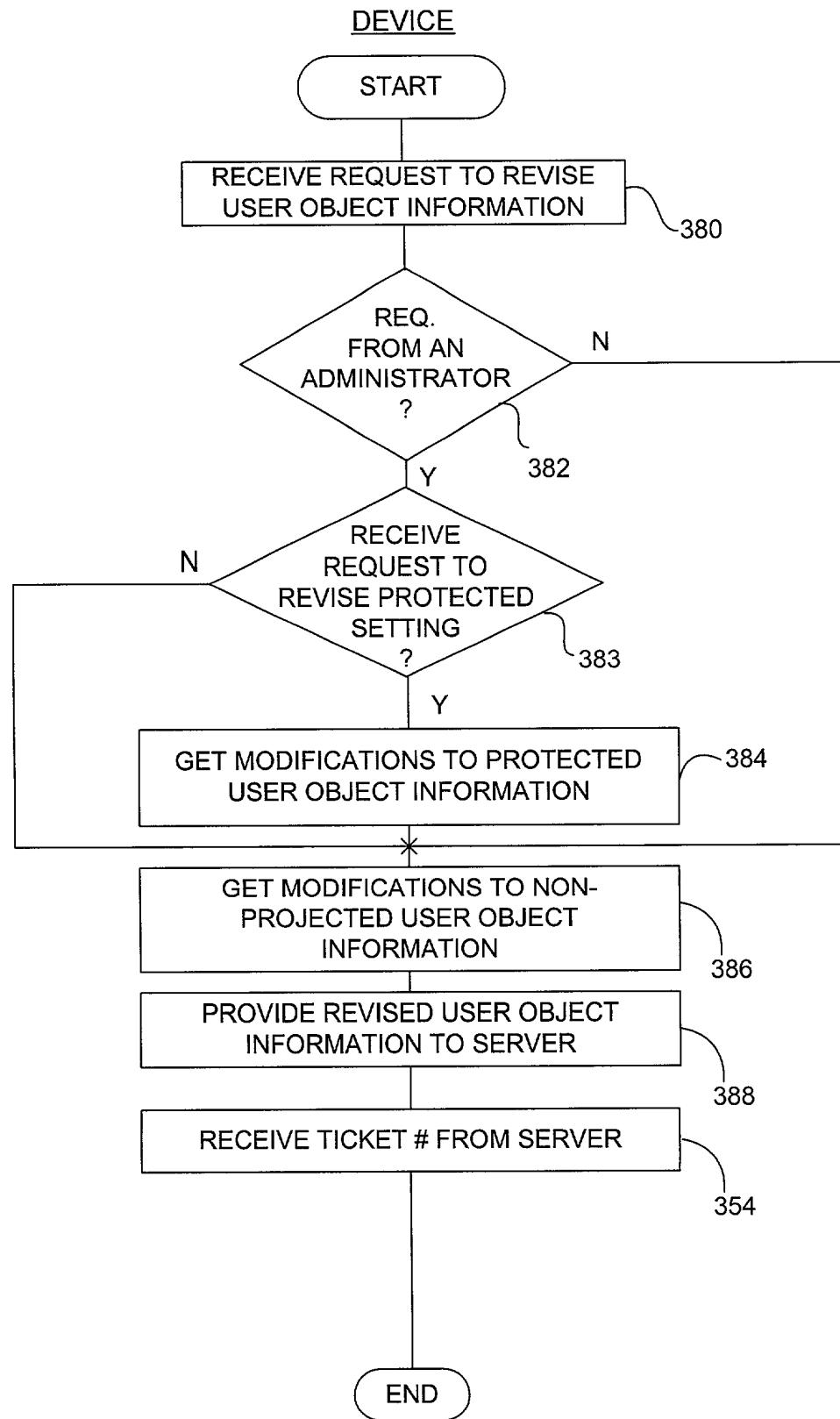


FIGURE 17

DEVICE

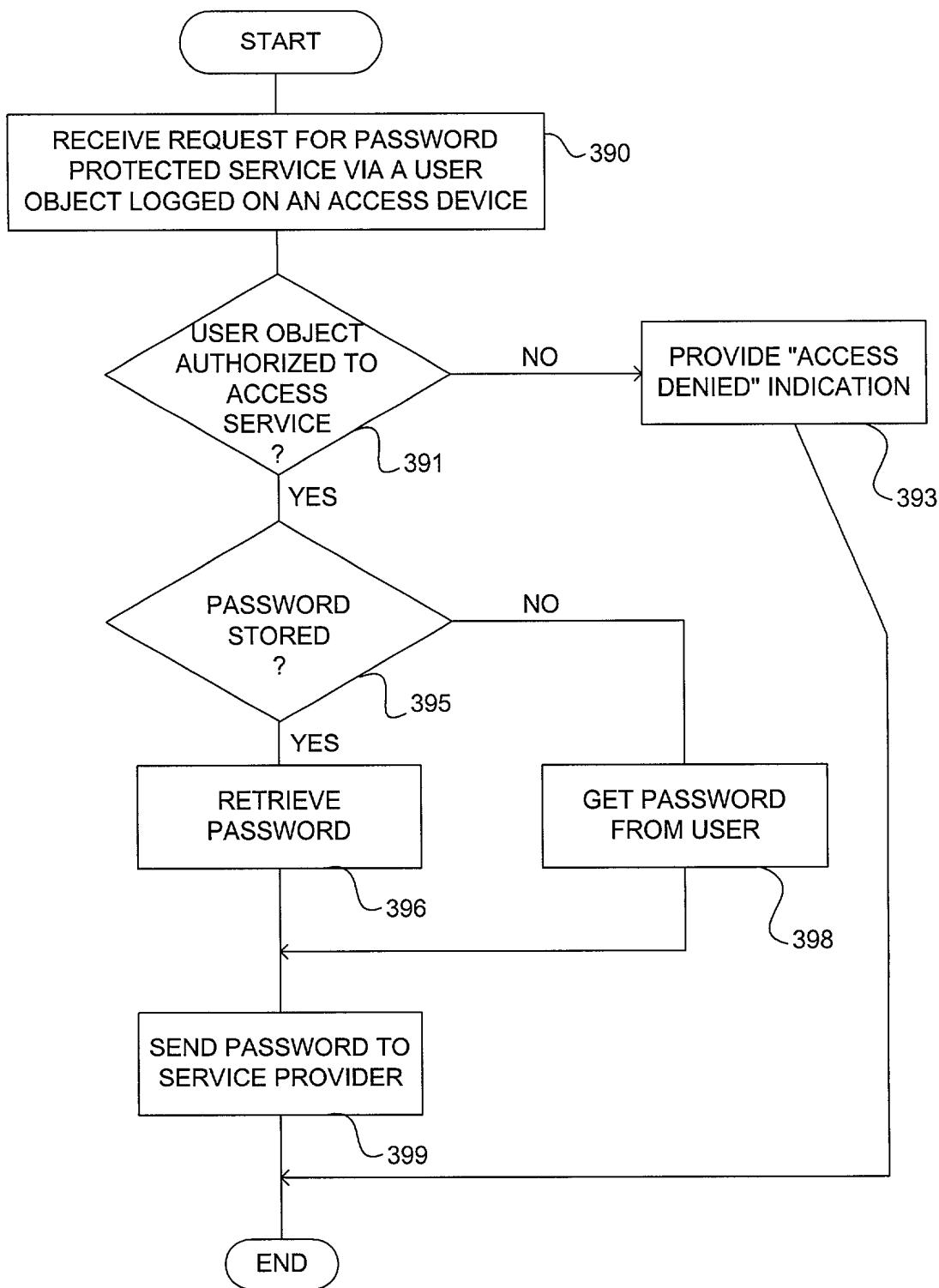


FIGURE 18